

## Verification Plan

### Overview

You will prepare a verification plan for each module in your design. You will use both black-box and clear-box testing methods. Your black-box tests will use vectors created from you module specifications. Your clear-box tests will be derived from the structure of the design.

For black-box:

- Generate a test that exercises each input for each distinct type of data (negative/positive, enumeration, *etc.*)
- For modules whose functions depend on mode inputs, generate a set of tests for each mode.

For clear-box:

- For a state-oriented design, generate tests to cover each path through the state graph and generate condition tests.
- For loops, generate loop tests.
- Generate a def-use test for each variable.

### You Should Turn In

A written report:

- For each module, give a state diagram specifying the algorithm (or actual code) and the black-box and clear-box tests.
- Give a class diagram and state diagrams for each method of any testbench code required for testing.